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NOTICE OF ALLOWANCE AND FEE(S) DUE

181

7590

03/11/2009

MILES & STOCKBRIDGE PC 1751 PINNACLE DRIVE SUITE 500 MCLEAN, VA 22102-3833 EXAMINER

COLE, BRANDON S

ART UNIT PAPER NUMBER

2816 DATE MAILED: 03/11/2009

APPLICA'	ΓΙΟΝ ΝΟ.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/58	4.395	06/23/2006	Keiko Fukuda	XA-10578	9000

TITLE OF INVENTION: VOLTAGE GENERATION CIRCUIT AND SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	06/11/2009

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

or <u>Fax</u> (571)-273-2885

INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where m

appropriate. All further indicated unless correct maintenance fee notifica	correspondence including delay or directed other tions.	ng the Patent, advance onerwise in Block 1, by (rders and notification of a) specifying a new corre	maintenance fees verspondence address	will be ; and/o	mailed to the current r (b) indicating a sepa	correspondence address as rate "FEE ADDRESS" for
CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address)				Note: A certificate of mailing can only be used for domestic mailings of the Fee(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission.			
181	7590 03/11	/2009	na				mission
MILES & STC 1751 PINNACL SUITE 500	I h Sta ado tra	Certificate of Mailing or Transmission I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelop addressed to the Mail Stop ISSUE FEE address above, or being facsimily transmitted to the USPTO (571) 273-2885, on the date indicated below.					
MCLEAN, VA	22102-3833						(Depositor's name)
							(Signature)
			L				(Date)
APPLICATION NO.	FILING DATE		FIRST NAMED INVENTO	R	ATTC	RNEY DOCKET NO.	CONFIRMATION NO.
10/584,395	06/23/2006	•	Keiko Fukuda		•	XA-10578	9000
TITLE OF INVENTION	: VOLTAGE GENERA	TION CIRCUIT AND SE	EMICONDUCTOR INTE	GRATED CIRCUI	ΓDEVI	CE	
APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSU	E FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0		\$1810	06/11/2009
EXAM	IINER	ART UNIT	CLASS-SUBCLASS	7			
COLE, BR	ANDON S	2816	327-541000	_			
1. Change of correspond CFR 1.363).	ence address or indicatio	n of "Fee Address" (37	2. For printing on the (1) the names of up t			nevs 1	
Change of corresp	ondence address (or Cha B/122) attached.	inge of Correspondence	or agents OR, alternat	ively,		•	
"Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.			(2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.				
3. ASSIGNEE NAME A	ND RESIDENCE DATA	A TO BE PRINTED ON	I THE PATENT (print or t	vpe)			
PLEASE NOTE: Unl	less an assignee is ident	ified below, no assignee		patent. If an assign	nee is io	dentified below, the do	ocument has been filed for
(A) NAME OF ASSI	GNEE		(B) RESIDENCE: (CIT	Y and STATE OR	COUNT	TRY)	
Please check the appropr	iate assignee category or	categories (will not be p	rinted on the patent): \Box	Individual 🗖 C	orporat	ion or other private gro	up entity 🚨 Government
4a. The following fee(s)	are submitted:	4	b. Payment of Fee(s): (Ple		ny prev	viously paid issue fee s	shown above)
Issue Fee	T 11 12	200 - 15	☐ A check is enclosed. ☐ Payment by credit card. Form PTO-2038 is attached.				
☐ Publication Fee (No small entity discount permitted)☐ Advance Order - # of Copies			The Director is hereby authorized to charge the required fee(s), any deficiency, or credit any				ficiency, or credit any
			overpayment, to Dep	osit Account Numb	er	(enclose ar	extra copy of this form).
5. Change in Entity Sta	tus (from status indicated s SMALL ENTITY state		☐ b. Applicant is no lo	nger claiming SMA	II EN'	TITV etatus See 37 CE	$ER = 1.27(\alpha)(2)$
NOTE: The Issue Fee an	d Publication Fee (if rea	uired) will not be accepte	d from anyone other than				e assignee or other party in
interest as shown by the	records of the United Sta	ites Patent and Trademark	COffice.				
Authorized Signature				Date			
Typed or printed name				_			
This collection of inform	nation is required by 37 C	CFR 1.311. The information	on is required to obtain or	retain a benefit by	the pub	lic which is to file (and	by the USPTO to process)
submitting the complete this form and/or suggest Box 1450, Alexandria, V Alexandria, Virginia 223	d application form to the ions for reducing this bu. Virginia 22313-1450. DC 113-1450.	e USPTO. Time will vary rden, should be sent to th O NOT SEND FEES OR	depending upon the indice Chief Information Office COMPLETED FORMS	ividual case. Any cocer, U.S. Patent and TO THIS ADDRES	ommen Trader S. SEN	ts on the amount of tin nark Office, U.S. Depa D TO: Commissioner f	g gathering, preparing, and ne you require to complete rtment of Commerce, P.O. or Patents, P.O. Box 1450,

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APPLICATION NO.	FILING DATE	FILING DATE FIRST NAMED INVENTOR		CONFIRMATION NO.
10/584,395	06/23/2006	Keiko Fukuda	XA-10578 9000	
181	7590 03/11/2009	EXAMINER		
MILES & STO	CKBRIDGE PC	COLE, BRANDON S		
1751 PINNACLE	DRIVE	ART UNIT	PAPER NUMBER	
SUITE 500 MCLEAN, VA 22102-3833			2816 DATE MAILED: 03/11/200	9

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 14 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 14 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 (571)-272-4200.

	Application No.	Applicant(s)	
	10/594 205	FUZUDA ET AL	
Notice of Allowability	10/584,395 Examiner	FUKUDA ET AL. Art Unit	
	DDANDON C COLE	2010	
	BRANDON S. COLE	2816	
The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in or other appropriate commun IGHTS. This application is su	this application. If not included nication will be mailed in due course	
1. X This communication is responsive to <u>amendments filed on</u>	January 8 th 2009.		
2. ☑ The allowed claim(s) is/are <u>7, 10 - 15, 17, and 18</u> .			
 3. Acknowledgment is made of a claim for foreign priority ur a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 	, , ,	· (f).	
Certified copies of the priority documents have	been received in Application	No	
3. Copies of the certified copies of the priority do	cuments have been received	in this national stage application fro	m the
International Bureau (PCT Rule 17.2(a)).			
* Certified copies not received:			
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		a reply complying with the requirement	ents
4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give			OF
5. CORRECTED DRAWINGS (as "replacement sheets") mus	st be submitted.		
(a) ☐ including changes required by the Notice of Draftspers	son's Patent Drawing Review	(PTO-948) attached	
1) 🔲 hereto or 2) 🔲 to Paper No./Mail Date			
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date	s Amendment / Comment or i	n the Office action of	
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t			of
6. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT			е
Attachment(s)	5 D Nation of Inf		
 Notice of References Cited (PTO-892) D Notice of Draftperson's Patent Drawing Review (PTO-948) 		ormal Patent Application	
•	6. ☐ Interview Su Paper No./N	lail Date	
 Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 10/06/2008 	/. ∐ Examiner's F	mendment/Comment	
4. Examiner's Comment Regarding Requirement for Deposit	8. 🛛 Examiner's S	statement of Reasons for Allowance	!
of Biological Material	9.		

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NOTICE OF ALLOWANCE

1. Allowance in response to amendments filed on 1/08/2009.

2. Claims 7, 10 -15, 17, and 18 are allowed.

Reasons for Allowance

3. The following is an examiner's statement of reasons for allowance:

Claims 7, 15, and 17 are allowable over the prior art of record because the art of record does not disclose or suggest obvious semiconductor integrated circuit device which includes a CMOS circuit having a second conductive-type well region and a first conductive-type well region formed on a first conductive-type semiconductor substrate, a first conductive-type MOSFET formed on the second conductive-type well region, and a second conductive-type MOSFET formed on the first conductive-type well region, wherein the second conductive-type well region electrically separates the first conductive-type well region on which the second conductive-type MOSFET is formed from the first conductive-type semiconductor substrate, and wherein each of the first transistor and the second transistor is formed of a bipolar transistor having a vertical structure with a second conductive-type diffusion layer, which is formed in a step for forming source and drain diffusion layers of the second conductive-type MOSFET, as an emitter portion, with a portion of a first conductive-type well arrangement, on which the second conductive-type diffusion layer constituting the emitter portion is formed, as

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a base portion, and with a portion of a second conductive-type well arrangement, which electrically separates said portion of the first conductive-type well arrangement from the first conductive-type semiconductor substrate, as a collector portion, in combination with the rest of the claimed limitations.

Claims 10 – 14, and 18 are allowable over the prior art of record because the art of record does not disclose or suggest obvious a semiconductor integrated circuit device includes a CMOS circuit having a second conductive-type well region and a first conductive-type well region formed on a first conductive-type semiconductor substrate, a first conductive-type MOSFET formed on the second conductive-type well region, and a second conductive-type MOSFET formed on the first conductive-type well region, wherein each of the first transistor and the second transistor of the reference voltage generating circuit is formed of a bipolar transistor having a lateral structure with collector, emitter, and base portions being constituted by diffusion layers formed on respective portions of a first conductive-type well arrangement, the diffusion layers of the collector and emitter portions being formed in a step for forming source and drain diffusion layers of the second conductive-type MOSFET, wherein the first conductivetype is a p-type and the second conducive-type is an n-type, wherein the power source voltage supplied from the second external terminal is positive, wherein the second transistor is constituted by one unit transistor, and wherein the first transistor is constituted by connecting a plurality of unit transistors corresponding to the second transistor in parallel, in combination with the rest of the claimed limitations.

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Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to BRANDON S. COLE whose telephone number is (571)270-5075. The examiner can normally be reached on Mon - Fri 7:30-5:00 EST (Alternate Friday's Off).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lincoln Donovan can be reached on (571) 272-1988. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Long Nguyen/ Primary Examiner, Art Unit 2816

/Brandon S Cole/ Examiner, Art Unit 2816